

Notice of References Cited

Application/Control No.

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Applicant(s)/Patent Under
Reexamination
SAKAI, JUNJI

Examiner

Mary J. Steelman

Art Unit

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,437,034	07-1995	Tanaka et al.	717/160
	B	US-5,598,561	01-1997	Funaki, Hiroshi	717/149
	C	US-6,449,711 B1	09-2002	Week, Jeremy	712/223
	D	US-6,588,009 B1	07-2003	Guffens et al.	717/161
	E	US-6,292,939 B1	09-2001	Itou et al.	717/149
	F	US-6,622,301 B1	09-2003	Hirooka et al.	717/149
	G	US-6,725,448 B1	04-2004	Moriya et al.	717/119
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Akkary, Haitham, Driscoll, Michael A., "A Dynamic Multithreading Processor", 1998 IEEE, p. 226-236, retrieved from IEEE database 4/22/2004.
	V	Blume, William and Eigenmann, Rudolf, "Performance Analysis of Parallelizing Compilers on the Perfect Benchmarks Programs", 1992 IEEE, p. 643-656, retrieved from IEEE database 4/22/2004.
	W	Chekuri, C., Johnson, R., Motwani, R., Natarajan, B., Rau, B.R., Schlansker, M., "Profile-Driven Instruction Level Parallel Scheduling with Application to Super Blocks", 1996 IEEE, p. 58-67, retrieved from IEEE database 4/22/2004.
	X	McKinley, Kathryn S., "A Compiler Optimization Algorithm for Shared-Memory Multiprocessors", 1998 IEEE, p. 769-787, retrieved from IEEE database 4/22/2004.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.